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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

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| **APPLICANTS: Hong Joon Ki; | | | | | |
| **CONTINUING DATA VERIFIED: | | | | | |
| ** FOREIGN APPLICATIONS VERIFIED: REPUBLIC OF KOREA 2001-52870 08/30/2001 | | | | | |
| PG-PUB DO NOT PUBLISH <input type="checkbox"/> | | | RESCIND <input type="checkbox"/> | | |
| Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no | | | ATTORNEY DOCKET NO | | |
| 35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no | | | 10729-P67426US0 | | |
| Verified and Acknowledged Examiners's initials | | | | | |
| TITLE : Stack chip module | | | | | |

U.S. DEPT. OF COMMERCE / PAT. & TM. PTO-436L (Rev. 12-04)

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| NOTICE OF ALLOWANCE MAILED | | CLAIMS ALLOWED | |
| | | Total Claims | Print Claim for O.G. |
| ISSUE FEE | | DRAWING | |
| Amount Due | Date Paid | Sheets Drwg. | Figs. Drwg. Print Fig. |
| | | Applicant Examiner | |
| <input type="checkbox"/> TERMINAL DISCLAIMER | | PREPARED FOR ISSUE | |
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